

Knobbe Martens Olson & Bear LLP

Intellectual Property Law



550 West C Street
Suite 1200
San Diego CA 92101
Tel 619-235-8550
Fax 619-235-0176
www.kmob.com

Eric M. Nelson

July 30, 2004

Examiner Frejd
United State Patent and Trademark Office
P.O. Box 1450
Alexandria VA 22313-1450

Re: Request for initialed PTO Form-1449
U.S. Patent Application
Title: **REUSE OF HARDWARE COMPONENTS**
Application No.: 09/273,089
Our Reference No.: IMEC65.1CPCP1

Dear Examiner Frejd:

This is to request that you initial for the uninitialed four patent documents on the PTO form-1449, filed on March 2, 2004 along with a request for continued examination (RCE). For your reference, I have enclosed a copy of the page in which the four patent documents are not initialed. I previously requested this on the July 19, 2004 conference call, but have not yet heard from you.

If the Examiner has any questions regarding this matter, he is respectfully invited to contact the undersigned at the telephone number indicated below.

Sincerely,

A handwritten signature in black ink, appearing to be "E. Nelson", written over a horizontal line.

Eric M. Nelson
(Tel) 619-687-8646

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072804

Orange County
949-760-0404

San Francisco
415-954-4114

Los Angeles
310-551-3450

Riverside
951-781-9231

San Luis Obispo
805-547-5580

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
IMEC65.10PCP1APPLICATION NO.
09/273,089INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT
Schaumont et al.FILING DATE
March 19, 1999GROUP
2123 2128

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	1	5,623,418	4/22/1997	Rosotker et al.	716	1	
	2	5,933,356	8/1999	Rostoker et al.	703	18	
	3	6,324,678	11/2001	Damgelo et al.	716	18	

Technology Center 2100

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	4	0 445 942 A2	2/25/1991	EP	—	—		

EXAMINER
INITIAL

OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

RF	5	Altmeyer, et al. "Generating ECAD Framework Code from Abstract Models," Proc. of the 32nd ACM/IEEE Conf. on Design Automation, 1995, pp. 88-93.
RF	6	Basu, et al. "DOORS: An Object-Oriented CAD System for High Level Synthesis," IEE Proceedings - Computers and Digital Techniques, 1997, Vol. 144, Issue 5, pp. 331-342.
RF	7	Bill Lin. "System Design Tools for Broadband Telecom Network Applications" XP-002114729, 1996, pp. 23-26.
RF	8	Bolsens, et al. "Hardware/Software Co-Design of Digital Telecommunications Systems," Proceedings of the IEEE, 1997, Vol. 85, Issue 3, pp. 391-418.
RF	9	Bredenfeld et al. "Tool Integration and Construction using General Graph-based Design Representations," Proc. of the 32nd ACM/IEEE Conf. on Design Automation, 1995.
RF	10	Burr, et al. "OPERAS in a DSP CAD Environment," Proceedings of the European Design Automation Conference, 1994, pp. 130-135.
RF	11	Chao, et al. "Efficient Retiming and Unfolding," IEEE Inter. Conf. on Acoustics, Speech and Signal Processing, 1993, Vol. 1, pp. 421-424.
RF	12	De Micheli, et al. "The Olympus Synthesis System," IEEE Design & Test of Computers, 1990, Vol. 7, Issue 5, pp. 37-53.
RF	13	Hylander and Meador. "Object Oriented VLSI Design Automation for Pulse Coded Neural Networks," 1994 IEEE International conference on Neural Networks, 1994, Vol. 3, pp. 1825-1829.

EXAMINER

Russell Freid

DATE CONSIDERED

5/3/04

*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.